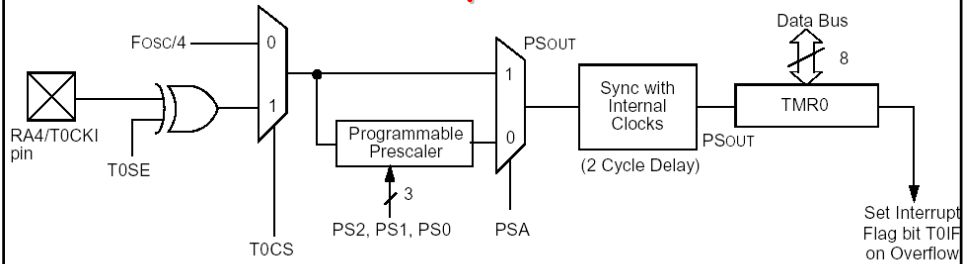


# Timer operation



Address	Name	Bit 7	Bit 6	Bit 5	Bit 4	Bit 3	Bit 2	Bit 1	Bit 0
01h	TMR0	Timer0 Module Register							
0Bh,8Bh	INTCON	GIE	EEIE	T0IE	INTE	RBIE	T0IF	INTF	RBIF
81h	OPTION_REG	RBP0	INTEDG	T0CS	T0SE	PSA	PS2	PS1	PS0
85h	TRISA	—	—	—	PORTA Data Direction Register				



Reset

org 0x00 ; Set program memory base at reset vector 0x00

goto Main ; Go to start of the main program

org 0x04 ; Interrupt vector

INCF PORTA,F ; Incrementa valor da porta A

movlw 0xF0

movwf TMR0 ; Init value to timer

bcf INTCON,T0IF

retfie ; return and enable interrupts

Main

BSF STATUS,RP0 ; Select Bank 1

CLRF TRISA ; Define PORTA as Output

BCF STATUS,RP0 ; Select Bank 0

movlw TEMPO

movwf TMR0 ; Init value to timer

BSF STATUS,RP0 ; Select Bank 1 (OPTION is on bank 1)

BSF OPTION\_REG,PS0 ; Set prescaler value to 4

BCF OPTION\_REG,PS1

BCF OPTION\_REG,PS2

BCF OPTION\_REG,PSA ; Not Uses the prescaler

BCF OPTION\_REG,T0CS ; Uses internal clock fosc/4

BCF STATUS,RP0 ; Select Bank 0

BSF INTCON,T0IE ; Enable Timer interrupt

BSF INTCON,GIE ; Enable interrupts

Repete

GOTO Repete ; Repete forever

