Splitting and merging

Rationality

Stream circuits

# Sampling, Splitting, and Merging in Coinductive Stream Calculus

### Jan Rutten, CWI & VUA

### Abstract

That this paper may stay out of the hands of the wonderfully cruel folklore referee who once said:

Your paper contains many new and interesting results. Unfortunately, the new results are not interesting, and the interesting results are not new ;-)

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- 1. Periodic stream samplers
- 2. Splitting and merging of streams
- 3. Rationality
- 4. Stream circuits

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### 1. Periodic stream samplers

- are relevant for stream processing systems and
- digital signal processing applications.

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### Examples of periodic stream samplers

• Let  $A^{\omega} = \{ \sigma \mid \sigma : \mathbb{N} \to A \}$  be the set of streams

 $\sigma = (\sigma(0), \sigma(1), \sigma(2), \ldots)$ 

• The function *even* :  $A^{\omega} \rightarrow A^{\omega}$  is given by

 $even(\sigma) = (\sigma(0), \sigma(2), \sigma(4), \ldots)$ 

• The drop operator  $D_4^2: A^\omega \to A^\omega$  is given by

 $D_4^2(\sigma) = (\sigma(0), \sigma(1), \sigma(3), \sigma(4), \sigma(5), \sigma(7), \ldots)$ 

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### Periodic stream samplers, traditionally

A monotone function *f* : N → N determines a *stream* sampler S<sub>f</sub> : A<sup>ω</sup> → A<sup>ω</sup> given by

 $S_f(\sigma)(n) = \sigma(f(n))$ 

- For which  $f : \mathbb{N} \to \mathbb{N}$  is  $S_f$  a *periodic* stream sampler?
- One formal answer [Mak 2005, TU/e]: periodic block maps.
- Tricky and non-intuitive; leads to difficult proofs.

### Periodic stream samplers, coinductively

- Recall:  $\sigma^{(k)} = (\sigma(k), \sigma(k+1), \sigma(k+2), ...).$
- A *periodic stream sampler*  $S : A^{\omega} \to A^{\omega}$  is defined by the following stream differential equation:

$$S(\sigma)^{(k)} = S(\sigma^{(l)})$$

We call l > 1 the *period* and  $1 \le k \le l$  the *block size*.

• Furthermore, we specify the initial values

 $S(\sigma)(0) = \sigma(n_0), \ldots, S(\sigma)(k-1) = \sigma(n_{k-1})$ 

where  $0 \le n_0 < n_1 < \cdots < n_{k-1} < l$ .

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### Periodic stream samplers, coinductively

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### Our earlier examples

• The function  $even : A^{\omega} \rightarrow A^{\omega}$  is given by

 $even(\sigma)^{(1)} = even(\sigma^{(2)}), \quad even(\sigma)(0) = \sigma(0)$ 

• The drop operator  $D_4^2: A^\omega o A^\omega$  is given by

 $D_4^2(\sigma)^{(3)} = D_4^2(\sigma^{(4)})$ 

with initial values

 $D_4^2(\sigma)(0) = \sigma(0), \ \ D_4^2(\sigma)(1) = \sigma(1), \ \ D_4^2(\sigma)(2) = \sigma(3)$ 

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### Easy proofs, coinductively

- If S, T : A<sup>ω</sup> → A<sup>ω</sup> are two periodic stream samplers then so is T ∘ S.
- We have:

 $D_2^0=D_4^0\circ D_5^2\circ D_6^4$ 

For a proof, define  $R \subseteq A^{\omega} \times A^{\omega}$  by

$$\begin{array}{rcl} {\it R} & = & \{ < D_2^0(\sigma), \; D_4^0 \circ D_5^2 \circ D_6^4(\sigma) \; > \mid \; \sigma \in {\it A}^{\omega} \; \} \\ & \cup & \{ < D_2^0(\sigma), \; D_4^2 \circ D_5^0 \circ D_6^2(\sigma) \; > \mid \; \sigma \in {\it A}^{\omega} \; \} \\ & \cup & \{ < D_2^0(\sigma), \; D_4^1 \circ D_5^3 \circ D_6^0(\sigma) \; > \mid \; \sigma \in {\it A}^{\omega} \; \} \end{array}$$

and note that it is a stream bisimulation.

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### 2. Splitting and merging of streams

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### Take and zip operators

- Example:  $T_3^2(\sigma) = (\sigma(2), \sigma(5), \sigma(8), ...)$
- General: we define the *take operator*  $T_{I}^{i}: A^{\omega} \rightarrow A^{\omega}$  by

 $T_l^i(\sigma)' = T_l^i(\sigma^{(l)}) \qquad T_l^i(\sigma)(0) = \sigma(i)$ 

for  $l \ge 2$  and  $0 \le i < l$ .

- Example:  $Z_2(\sigma, \tau) = (\sigma(0), \tau(0), \sigma(1), \tau(1), \sigma(2), \tau(2), \ldots)$
- General: we define the *zip operator*  $Z_k : (A^{\omega})^k \to A^{\omega}$  by

 $Z_k(\sigma_0,\ldots,\sigma_{k-1})'=Z_k(\sigma_1,\ldots,\sigma_{k-1},\sigma_0')$ 

with initial value  $Z_k(\sigma_0,\ldots,\sigma_{k-1})(0) = \sigma_0(0)$ .

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### Take and zip operators

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$$Z_k(\sigma_0,\ldots,\sigma_{k-1})'=Z_k(\sigma_1,\ldots,\sigma_{k-1},\sigma_0')$$

with initial value  $Z_k(\sigma_0, \ldots, \sigma_{k-1})(0) = \sigma_0(0)$ .

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### Take and zip operators

- Take and zip let us split and merge streams.
- They are sufficient to express all periodic stream samplers  $S: A^{\omega} \rightarrow A^{\omega}$ :

 $S(\sigma) = Z_k(T_l^{n_0}(\sigma), T_l^{n_1}(\sigma), \ldots, T_l^{n_{k-1}}(\sigma))$ 

of period l > 1 and block size  $1 \le k \le l$ .

For instance,

 $D_{6}^{4}(\sigma) = \ Z_{5}(\ T_{6}^{0}(\sigma),\ T_{6}^{1}(\sigma),\ T_{6}^{2}(\sigma),\ T_{6}^{3}(\sigma),\ T_{6}^{5}(\sigma))$ 

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### Take and zip operators

• More generally, we can define periodic stream *transformers*, not only *samplers*. E.g.,

 $Rev_{3}(\sigma) = (\sigma(2), \sigma(1), \sigma(0), \sigma(5), \sigma(4), \sigma(3), \ldots)$ 

which is given by

 $Rev_3(\sigma) = Z_3(T_3^2(\sigma), T_3^1(\sigma), T_3^0(\sigma))$ 

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### **Basic properties**

• Take and zip are each other's *inverse*, as follows:

 $Z_k(T_k^0(\sigma), \ldots, T_k^{k-1}(\sigma)) = \sigma$  $T_l^i(Z_l(\sigma_0, \ldots, \sigma_{l-1})) = \sigma_i$ 

Also,

$$T_{l}^{i}(\sigma) = Z_{k}(T_{k \times l}^{i}(\sigma), T_{k \times l}^{l+i}(\sigma), \ldots, T_{k \times l}^{(k-1) \times l+i}(\sigma))$$

• One can do elementary equational reasoning using these (and similar) laws.

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### 3. Rational streams

- are finitely representable, for instance by: stream circuits, weighted automata, vector spaces of finite dimension.
- cf. rational languages.

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### Stream calculus (on $\mathbb{R}^{\omega}$ )

We use the following constants and operators:

- constants (one for each  $r \in \mathbb{R}$ ): [r] = (r, 0, 0, 0, ...)
- constant (cf. formal variable): *X* = (0, 1, 0, 0, 0, ...)
- sum:  $(\sigma + \tau)(\mathbf{n}) = \sigma(\mathbf{n}) + \tau(\mathbf{n})$
- convolution (aka Cauchy) product:

$$(\sigma \times \tau)(n) = \sigma(0) \cdot \tau(n) + \cdots + \sigma(n) \cdot \tau(0)$$

• (formal) inverse to product: if  $\sigma(0) \neq 0$  then  $\exists ! \frac{1}{\sigma}$  s.t.

$$\sigma\times\frac{1}{\sigma}=(1,\,0,\,0,\,0,\ldots)$$

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### Stream calculus (on $\mathbb{R}^{\omega}$ )

Note:

$$X \times \sigma = (\mathbf{0}, \, \sigma(\mathbf{0}), \, \sigma(\mathbf{1}), \, \sigma(\mathbf{2}), \, \dots)$$

Convention:

 $3 \times X^2 = [3] \times X \times X$  (= (0, 0, 3, 0, 0, 0, ...))

### • Polynomial streams: for instance,

 $2+3X-7X^4$  (= (2, 3, 0, 0, -7, 0, 0, 0, ...))

### • Rational streams: for instance,

$$\frac{1+X}{1-2X+X^2} \quad (= (1, 3, 5, 7, \ldots))$$

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### *Take* and *zip* preserve rationality

An elementary proof is based on the following facts (cf. [BR88]):

• For all  $k \ge 1$ ,

$$Z_k(\sigma_0,\ldots,\sigma_{k-1}) = \sigma_0(X^k) + (X \times \sigma_1(X^k)) + \cdots + (X^{k-1} \times \sigma_{k-1}(X^k))$$

•  $T_l^i$  is linear: for all  $r, s \in \mathbb{R}, \sigma, \tau \in \mathbb{R}^{\omega}$ ,  $T_l^i((s \times \sigma) + (t \times \tau)) = (s \times T_l^i(\sigma)) + (t \times T_l^i(\tau))$ 

• For 
$$1 \le i \le l$$
 and  $\sigma \in \mathbb{R}^{\omega}$ ,  
 $T_l^i(X \times \sigma) = T_l^{i-1}(\sigma)$   $T_l^0(X \times \sigma) = X \times T_l^{l-1}(\sigma)$ 

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### Take and zip preserve rationality

For instance, for

$$\sigma = \frac{1}{(1-X)^2} = (1,2,3,\ldots)$$

we have:

$$T_3^0(\sigma) = \frac{1+2X}{(1-X)^2} \qquad T_3^1(\sigma) = \frac{2+X}{(1-X)^2} \qquad T_3^2(\sigma) = \frac{3}{(1-X)^2}$$

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### Take and zip preserve rationality

For instance, for

$$\sigma = \frac{1}{(1-X)^2} = (1,2,3,\ldots)$$

we have:

$$\begin{aligned} \textit{Rev}_{3}(\sigma) &= Z_{3}(T_{3}^{2}(\sigma), T_{3}^{1}(\sigma), T_{3}^{0}(\sigma)) \\ &= Z_{3}\left(\frac{3}{(1-X)^{2}}, \frac{2+X}{(1-X)^{2}}, \frac{1+2X}{(1-X)^{2}}\right) \\ &= \frac{3}{(1-X^{3})^{2}} + X \times \frac{2+X^{3}}{(1-X^{3})^{2}} + X^{2} \times \frac{1+2X^{3}}{(1-X^{3})^{2}} \\ &= \frac{3-X-X^{2}+2X^{3}}{(1-X)^{2}(1+X+X^{2})} \end{aligned}$$

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### 4. Stream circuits

- Finite stream circuits built from adders and registers correspond to rational streams.
- Here we want to add *take* and *zip* gates and see what happens.

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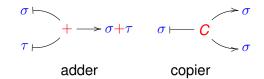
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### Behaviour of stream circuits

• Four basic types of gates:

*r*-multiplier:  $\sigma \vdash \stackrel{r}{\longrightarrow} r \times \sigma$ 

register:  $\sigma \longmapsto \mathbf{r} + (\mathbf{X} \times \sigma) \quad (= (\mathbf{r}, \sigma(\mathbf{0}), \sigma(\mathbf{1}), \ldots))$ 



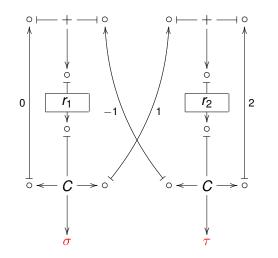
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### Computing streams $\sigma$ and $\tau$



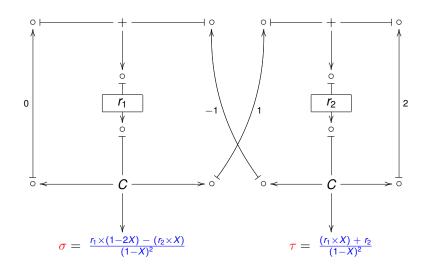
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### Two rational streams:



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### Extended stream circuits

• A new type of gate for each *take* operator:

$$\sigma \longmapsto T_{l}^{i} \longrightarrow T_{l}^{i}(\sigma)$$

• A new type of gate for each *zip* operator:

