Hands on a Grand Challenge in Computing: Proving a Journaled File System Correct

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Context

Opening questions

- Are we doing computer science research in the right way?
- Are we using the right notation, language?
- Does more technology mean better science?
- "Is computer science science?" (Denning, 2005)

Science? Pre-science?

In an excellent book on the history of scientific technology, "How Science Was Born in 300BC and Why It Had to Be Reborn" (Springer, 2003),

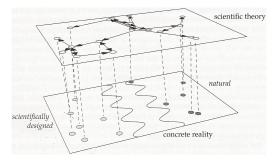
Lucio Russo writes:

The immense usefulness of exact science consists in providing **models** of the real world within which there is a guaranteed method for telling false statements from true. (...) Such models, of course, allow one to describe and **predict** natural phenomena, by translating them to the theoretical level via **correspondence rules**, then solving the "exercises" thus obtained and translating the solutions obtained back to the real world.

Disciplines unable to build themselves around *"exercises"* are regarded as **pre-scientific**.

Scientific engineering (e = m + c)

Also from Russo's book :



Vertical lines mean abstraction, horizontal ones mean calculation:

$$\underline{e}ngineering = \underline{m}odel \text{ first, then } \underline{c}alculate$$
$$(e = m + c)$$

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Donald Knuth:

My experience has been that **theories** are often more structured and interesting when they are based on **real problems**; somehow they are more exciting than completely abstract theories will ever be.

(Quoted from *The Dangers of Computer-science Theory*. Standford University, 1971)

This kind of position explains the **Grand Challenges in Computing** initiative.

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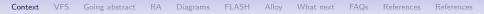
Context VFS Going abstract RA Diagrams FLASH Alloy What next FAQs References References

Grand Challenge Initiative

- Healthy trend in **formal methods** (FM) research driven by the idea of a **Grand Challenge** (GC).
- Triggered by eminent computer scientists T. Hoare & J. Misra.
- VSTTE conference ("Verified Software: Theories, Tools, Experiments") created as response to the challenge.
- VSTTE'05: Hoare proposes that time to start long term international cooperation research projects has arrived.
- Outcome to be gathered in a Verified Software Repository (VSR).

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• No funding.



Verified Software Repository

Mondex — A verified electronic purse hosted on a smart card. **Players**: Bremen (OCL); Escher Technologies (PerfectDeveloper); MIT (Alloy); Macao/DTU (Raise); Newcastle (p-Calculus); Southampton (Event-B); York (Z).

Pacemaker — based on a previous generation pacemaker specification released by Boston Scientific (BSC). Aims at production of verified pacemaker software, designed to run on specified PIC hardware.
 Players (thus far): Aharus (VDM++); BSC (BLESS); UFRGN (Z, PerfectDeveloper). UPEN (Uppaal, ADL).

Verified Software Repository

Verified File System (VFS)

— Verified subset of POSIX suitable for flash-memory hardware with strict fault-tolerant requirements to be used by forthcoming NASA's JPL missions.

Players (thus far): Augsburg (KIV); MIT (Alloy); Minho (Alloy etc); Southampton (Event-B); York (Z/Eves).



VFS

VFS @ Minho

First effort was concerned with verifying Intel[®] Flash File System Core Reference Guide (API):

File System API Reference

int_

4.6 FS DeleteFileDir

Deletes a single file/directory from the media

Syntax

FFS_Status	FS	DeleteFileDir (
mOS_	char	*full_path,
UINT8		static_info_type);

Parameters

Parameter	Description	
*full_path	(IN) This is the full path of the filename for the file or directory to be deleted.	
static_info_type	(IN) This tells whether this function is called to delete a file or a directory.	

Error Codes/Return Values

FFS_StatusSuccess	Success
FFS_StatusNotInitialized	Failure
FFS_StatusInvalidPath	Failure
FFS_StatusInvalidTarget	Failure
FFS_StatusFileStillOpen	Failure

(Permission to reproduce this excerpt kindly granted by Intel Corporation.) < □ > < 同 > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > < □ > <



VFS @ Minho

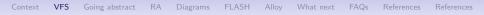
Formal model unveiled some ambiguities in the documentation, eg.

• Can the root directory be removed?

Surprised to see the POSIX System Interface Standard (2004) itself vague in this respect:

The rmdir() function shall remove a directory whose name is given by path. The directory shall be removed only if it is an empty directory. If the directory is the root directory or the current working directory of any process, it is unspecified whether the function succeeds, or whether it shall fail and set errno to [EBUSY].

Publications: see (Oliveira, 2009), (Ferreira and Oliveira, 2009)



VFS @ Minho (recent)

GCI still suffering from lack of comparative work:

- We've chosen the KIV Augsburg model (Schierl et al., 2009) to compare our work with.
- Alloy emulation of Augsburg model subject of a Master thesis by Fernandes (2010) available soon.
- Going abstract: high-level model in Alloy of the most interesting part of KIV model, which has to do with the **journaling**, **wear leveling** and **power loss** recovery mechanisms.
- Formal design and calculational approach (as explained later in this talk)

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VFS @ Augsburg (KIV)

- Standard: They adhere to UBIFS (Unsorted Block Image File System) — a journaled file system developed by Nokia + Univ. Szeged that works on top of UBI (a wear-leveling and volume management system for flash devices).
- **Tool:** Karlsruhe Interactive Verifier (KIV) a tactical theorem prover developed at the Univ. of Karlsruhe.

Main source: a nice paper

A. Schierl, G. Schellhorn, D. Haneberg, W. Reif Abstract specification of the UBIFS file system for flash memory. LNCS volume 5850, pages 190–206. Springer, 2009.

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supported by a very detailed website: www.informatik.uni-augsburg.de/swt/projects/flash.html



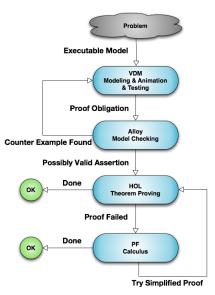
Verification life-cycle made of several steps:

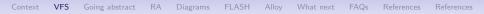
- Build and animate the file system model (VDM++)
- Automatic generation of verification proof obligations (PO)

- PO model-checking step (Alloy)
- PO discharge step (HOL)

(Diagram next slide.)

Our (first) approach





What we have learnt

Mea culpa:

- Too technological
- Too many tools
- Tool interoperability at target in the GCI but hard to accomplish in practice
- Even if successful technology-wise: "push-button proofs (alone) considered harmful"
- Lack of *proof awareness* proofs with too many (often hundreds, thousands) of steps.

Questions:

• How to reduce such complex models and proofs to something *small (readable) and elegant?*

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Abstract modeling

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• Abstraction

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My current answer to such needs is the

Relation algebra (RA)

which underlies the Algebra of Programming (Bird and de Moor, 1997). Why?

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- Abstract models capture nothing but the *essence* of given problems expressed in terms of **relationships** among **objects** of interest.
- So, relational models are natural and stem from natural language itself, cf. sentences such as eg.

John loves Mary

of the same shape as mathematical relationship

$0 \leq 1$

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("0 is at most 1"), and so on. (Note the infix notation.)



Why Relation Algebra (RA)

- The **algebra** of binary relations replaces logic deduction by inequational reasoning.
- Such calculations are **pointfree**, saving ink by dropping variables, quantifiers, variable substitution etc.
- Such was the motivation of mathematicians like Alfred Tarski (1901-83) who had a life-long struggle with quantified notation (too complex for his needs).

Why Relation Algebra (RA)

A bit of history:

• **RA** actually the first attempt to formal **knowledge representation**, carried out by Augustus de Morgan (1806-71) in his

On the syllogism: IV, and on the logic of relations

read on April 23, 1860 to the the Cambridge Philosophical Society. This **predates** logic itself.

- In fact, Peirce (1839-1914) invented *quantifier* notation to explain de Morgan's algebra of relations
- De Morgan's pioneering work was ill fated: the language invented to explain his **RA** became eventually more popular than **RA** itself.
- Tarski was among those who revived **RA**.

On the syllogism: IV(...)

Binary relations:

[...] Let X..LY signify that X is some one of the objects of thought which stand to Y in the relation L, or is one of the Ls of Y.

Relational composition:

[...] When the predicate is itself the subject of a relation, there may be a **composition**: thus if X..L(MY), if X be one of the Ls of one of the M s of Y, we may think of X as an 'L of M' of Y, expressed by X..(LM)Y, or simply by X..LMY. [...][So] brother of parent is identical with uncle, by mere definition.

Relational converse:

[...] The **converse** relation of L, L^{-1} , is defined as usual: if X.. L Y, Y ... L^{-1} X : if X be one of the Ls of Y, Y is one of the L^{-1} s of X.

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Context VFS Going abstract RA Diagrams FLASH Alloy What next FAQs References References Binary relation essentials

Binary relations are typed:

Arrow $A \xrightarrow{R} B$ denotes a binary relation from A (source) to B (target), where A, B are **types**. Writing $B \xleftarrow{R} A$ means the same as $A \xrightarrow{R} B$.

Infix notation (such as verbs in natural language), eg.:

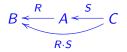
John Loves Mary

 $0 \le \pi$ b R a (in general)

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(See Freyd and Scedrov (1990) for the foundations of typed RA.)

Composition "... is *R* of some *S* of...":



 $b(R \cdot S)c \Leftrightarrow \langle \exists a :: b R a \land a S c \rangle$ (1)

Converse of *R* (or *R* in "passive form"): for all *a*, *b*,

$$A \stackrel{R^{\circ}}{\longleftarrow} B \qquad B \stackrel{R}{\longleftarrow} A$$
$$a(R^{\circ})b \Leftrightarrow b R a \qquad (2)$$

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Note how (1) removes \exists when applied from right to left.

Context VFS Going abstract RA Diagrams FLASH Alloy What next FAQs References References Polymorphic relations

Тор

 $B \leftarrow A$ is the largest relation of its type — $b \top a$ always holds (this is de Morgan's "is coexistent with" relation)

Bottom

 $B \stackrel{\perp}{\longleftarrow} A$ is the smallest relation of its type — $b \perp a$ is always false

Identity

 $A \stackrel{id}{\longleftarrow} A$ is the smallest **equivalence** relation of its type — b id a holds iff b = a

The "at most" ordering

Each type $B \leftarrow A$ forms a complete Boolean algebra (briefly speaking), whose ordering captures universal quantification:

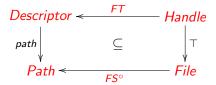
$$R \subseteq S \iff \langle \forall b, a : b R a : b S a \rangle$$
(3)

Comments:

- Read $R \subseteq S$ as "*R* is at most *S*", that is, "all *R*s are *S*s".
- Note how (3) *removes* ∀ when applied from right to left. ("Pointfree" transform).
- What we have thus far is enough for much abstract modelling. . .
- Tarski would say: *"Isn't [this] a nice thing 'pour épater les logiciens-bourgeois'?"* (Givant, 2006)



Since relations are arrows, we can draw **diagrams** describing **constraints**, for instance



depicting constraint

$$path \cdot FT \subseteq FS^{\circ} \cdot \top \tag{4}$$

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where FS is a *file store*, FT is the open-file table and *path* yields the path of an open file descriptor. What does (4) mean, in predicate logic? See next slide.

From the "pointfree" (RA) to the "pointwise" (FOL)

We calculate:

 $path \cdot FT \subseteq FS^{\circ} \cdot \top$ $\Leftrightarrow \qquad \{ \text{ 'at most' ordering (3)} \}$ $\langle \forall p, h : p(path \cdot FT)h : p(FS^{\circ} \cdot \top)h \rangle$ $\Leftrightarrow \qquad \{ \text{ composition (1)} \}$ $\langle \forall p, h : \langle \exists d :: d FT h \land p = path d \rangle : p(FS^{\circ} \cdot \top)h \rangle$

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We are left with $p(FS^{\circ} \cdot \top)h$ — see next slide.

From the "pointfree" (RA) to the "pointwise" (FOL)

$$p(FS^{\circ} \cdot \top)h$$

$$\Leftrightarrow \qquad \{ \text{ composition again (1) } \}$$

$$\langle \exists x :: p(FS^{\circ})x \land x \top h \rangle$$

$$\Leftrightarrow \qquad \{ \text{ converse }; x \top h \text{ always holds } \}$$

$$\langle \exists x :: x FS p \land \text{TRUE} \rangle$$

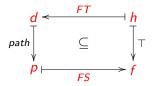
$$\Leftrightarrow \qquad \{ \text{ trivia } \}$$

$$\langle \exists x :: x FS p \rangle$$

Altogether, *path* \cdot *FT* \subseteq *FS* $^{\circ} \cdot \top$ unfolds into (next slide):

From the "pointfree" (RA) to the "pointwise" (FOL)

 $\langle \forall p, h : \langle \exists d :: d FT h \land p = path d \rangle : \langle \exists f :: f FS p \rangle \rangle$ Informally:



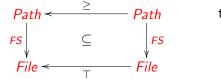
If h is the handle of a open-file descriptor d holding path p, then p points to some existing file f.

In short:

Non-existing files cannot be opened. (Referential integrity)

Model constraints are diagrams

Another example:



that is $FS \cdot \ge \subseteq \top \cdot FS$

where $p \le p'$ means p is a sub-path of p' and whose meaning is Mother-directories always exist.

Summary:

Properties such as **referential integrity**, prefix-closure and many others are captured by easy-to-grasp **RA** expressions depicted by diagrams.

Model constraints are diagrams

Other examples (in general):

• *M*, *N* domain-disjoint:

 $M \cdot N^{\circ} \subseteq \bot$

• *M*, *N* domain-coherent:

 $M \cdot N^{\circ} \subseteq id$

• Domain of *M* strictly above that of *N*:

 $M^{\circ} \cdot \top \cdot N \subseteq >$

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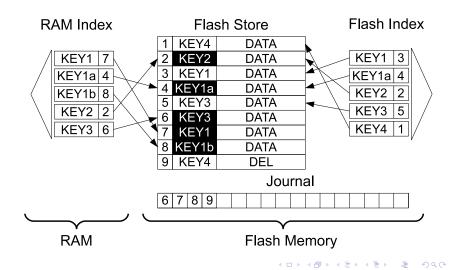
Flash file store refinement

The model of a flash-memory file-store is far more complex than what has been hinted above, given extra non-functional requirements such as:

- **Performance:** core meta-data is stored in central memory (RAM) to decrease update latency.
- Wear leveling: no delete/write cycles so as to prolong the service life of this erasable storage media.
- **Power-loss recovery:** was a way to add robustness to the system against faults of this kind, a *backlog* (**journal**) of the operations that have been performed is stored in the device.

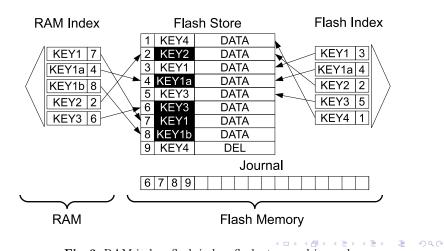
Journaled FLASH store snapshot

In a picture (quoted from Schierl et al. (2009)):



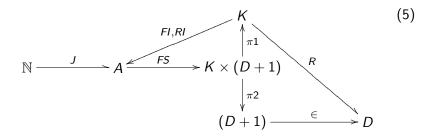
Journaled file store snapshot

Note how **updating** (eg. key *KEY3*) and **deletion** (eg. key *KEY4*) actually entail **new** entries in the FLASH (thus the counter-intuitive fact that deletion calls for extra free space):



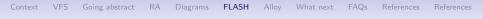
FLASH abstract model in a diagram

Generic model

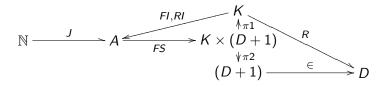


where A (memory addresses), K (keys) and D (data) can be regarded at any level — (eg. K = Path, K = inode number, etc).

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Abstract model in a diagram



Concrete states:

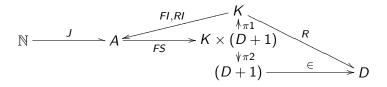
- FS FLASH store
- RI RAM index
- FI FLASH index
- J Journal (sequence of addresses)

Abstract states:

R — Abstract *K*-*D* relationship being implemented.

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Abstract model in a diagram



Data types:

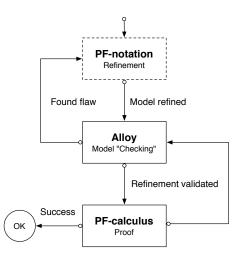
D + 1 — accommodates both valid data (D) and the DEL mark (1) intended for recording data deletion. Membership relation $d \in x$ picks data from a non-DEL entry x.

 $K \times (D+1)$ — accommodates pairs (k, d) of keys and (maybe) data; projections π_1 and π_2 such that $\pi_1(k, d) = k$ and $\pi_2(k, d) = d$. Context VFS Going abstract RA Diagrams FLASH Alloy What next FAQs References Reference Alloy

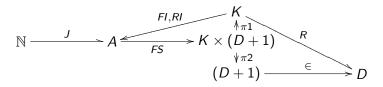
Bringing Alloy in - why?

- Need to obtain **feedback** about our model
- Need to know as soon as possible if building an inconsistent model
- Counter-examples very useful in case of nonsense proof-obligations
- Alloy's **pointfree** subset is very close to **RA**

Thus the *minimalist* verification life-cycle on the right:



Abstract model diagram in Alloy



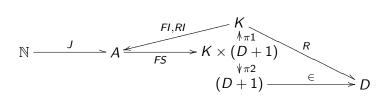
Abstract states:

 $\textbf{sig} \ AS \ \{ \ r : K \rightarrow \textbf{lone} \ D \ \}$

Concrete states:

$$\begin{array}{l} \text{sig CS } \{ \\ j: N \rightarrow \text{lone } A, \\ fs: A \rightarrow \text{lone Entry}, \\ fi: K \rightarrow \text{lone } A, \\ ri: K \rightarrow \text{lone } A \\ \} \end{array}$$





 $K \times (D+1)$

sig Entry { key : one K, value : one DataCell }

D+1

```
abstract sig DataCell {}
one sig Del extends DataCell {}
sig Data extends DataCell { data: one D }
```

Multiplicities in Alloy + taxonomy

A lone -> B	A -> some B		A -> lone B		A some -> B		
injective	entire		simple		surjective		
A lone -> some B A ->			one B	A some -> lone B			
representation func		tion abstraction		abstraction			
A lone -> one B			A some -> one B				
injection			surjection				
A one -> one B							
bijection							

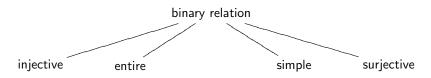
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where

- lone at most one
- some at least one
 - one exactly one

The same — mathematically

Topmost criteria:



Definitions:

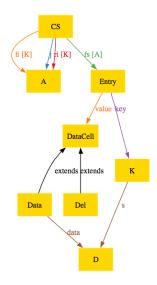
	⊇ id	\subseteq id		
ker <i>R</i>	entire R	injective <i>R</i>		
img R	surjective R	simple <i>R</i>		

 $\ker R = R^{\circ} \cdot R$ $\operatorname{img} R = R \cdot R^{\circ}$

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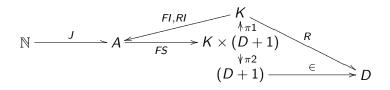
Abstract model as depicted by Alloy



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Abstraction invariant



R = af(FS, FI, RI, J)

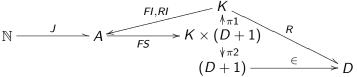
where abstraction function is

 $af(FS, FI, RI, J) \triangleq (active FS) \cdot RI$ (6)

for

$$A \xrightarrow{\text{active FS}} D \quad \triangleq \quad \in \cdot \pi_2 \cdot FS \tag{7}$$

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Abstraction function $af(FS, FI, RI, J) \triangleq (active FS) \cdot RI$

fun af[cs: CS] : $K \rightarrow D \{ (cs \cdot ri) \cdot (cs \cdot fs \cdot active) \}$

Auxiliary function *active* $FS \triangleq \in \pi_2 \cdot FS$

fun active[x: CS·fs] : A \rightarrow D { x·value·data }

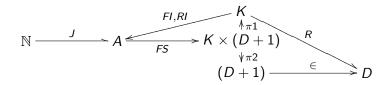
(Note that reverse order in which Alloy chains the arguments of relational composition.)

Alloy

Recall the class of simple relations (partial functions), where $A \xrightarrow{S} B$ is simple iff $S \cdot S^{\circ} \subseteq id$ which, once variables are added, means

 $\langle \forall b, b' : \langle \exists a :: bSa \land b'Sa \rangle : b = b' \rangle$

(=S is univocal, deterministic). Clearly, we want FS, J, RI, FI simple.



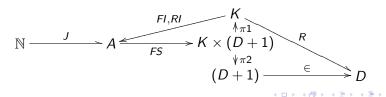
Recall FLASH non-functional requirements such as wear leveling and **power loss** recovery. In the event of a power loss RI will be lost. The redundancy of FS (5) is intended for recovery, provided **consistency** clause

$$RI \subseteq index FS$$
 (8)

is added to the concrete invariant, where

$$K \xrightarrow{index FS} A \triangleq (\pi_1 \cdot FS)^{\circ}$$
(9)

 $\textbf{fun} \ index[x: \ CS{\cdot}fs] : K \ \rightarrow \ A \ \left\{ \ \tilde{\ }(x{\cdot}key) \ \right\}$

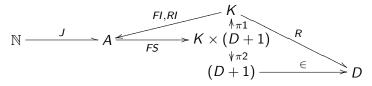


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Recovery possible only if there is a function which **rebuilds** *RI* from the other, persistent (FLASH-stored) relations. First attempt:

RI = index FS

Doesn't work! *index* $FS = (\pi_1 \cdot FS)^\circ$ is in general injective but not simple. Why? Keeping track of all addresses which have been involved in recording data for a given k, information is missing about which addresses correspond to the **most recent** updates.



Rules of thumb:

- 1. R° simple iff R injective
- 2. R° entire iff R surjective

Bringing journal into the play

We need to reduce the non-determinism of index FS by selecting only the most recent updates. This is where the **journal** helps,

$$A \stackrel{\geq_J}{\longleftarrow} A \triangleq J \cdot \geq \cdot J^{\circ} \tag{10}$$

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ordering addresses by comparing their relative positions in J, larger positions meaning more recent updates:

 $a \ge_J b \quad \Leftrightarrow \quad \langle \exists t, t' :: aJt \land bJt' \land t \ge t' \rangle$

Then we use the relational **"shrink" combinator** to express the selection of the most recent update per key:

 $RI = index FS \upharpoonright (\geq_J)$

Let us explain what it means.

Context VFS Going abstract RA Diagrams FLASH Alloy What next FAQs References References

Intuition about $R \upharpoonright S$

Example of $R \upharpoonright S$ in data-processing:

(Mark	Student \		Mark	Student
10	John	= ≤1	IVIAIN	
			11	Mary
11	Mary		12 15	John Arthur
12	John			
\ 15	Arthur /			

Example of $R \upharpoonright S$ in list-processing: given a sequence $N \xrightarrow{S} A$,

$$N \xrightarrow{\operatorname{nub} S} A \triangleq (S^{\circ} \upharpoonright \leq)^{\circ}$$

removes all duplicates while keeping the first instances. (N could be regarded as a time stamp.)

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Definition of $R \upharpoonright S$

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Given relation $B \stackrel{R}{\longleftarrow} A$ and optimization criterion $B \stackrel{S}{\longleftarrow} B$ on its outputs,



define $R \upharpoonright S$ satisfying universal property:

 $X \subseteq R \upharpoonright S \quad \Leftrightarrow \quad X \subseteq R \land X \cdot R^{\circ} \subseteq S \tag{11}$

This ensures $R \upharpoonright S$ as the largest sub-relation X of R such that, for all $b', b \in B$, if there exists $a \in A$ such that $b'Xa \land bRa$, then b'Sb holds ("b' better than b").



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Chaotic optimization:

 $R \upharpoonright \top = R \tag{12}$

Impossible optimization:

$$R \upharpoonright \bot = \bot \tag{13}$$

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Ensure simplicity (determinism):

 $R \upharpoonright S$ is simple $\Leftarrow S$ is anti-symmetric (14)

Select determinism:

 $R \mid id =$ largest deterministic fragment of R (15)

Context VFS Going abstract RA Diagrams FLASH Alloy What next FAQs References References Gaining what?

- No (explicit) recursion. (Hidden in the $R \upharpoonright S$ combinator.)
- No need for inductive proofs.
- Deductive calculation as in high school algebra, thanks to distributive, permutative properties, eg.

 $(R \cup S) \upharpoonright U = (R \upharpoonright U) \cup (S \upharpoonright U) \iff R \cdot S^{\circ} \subseteq \bot$

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• Theory reusable elsewhere (eg. currently using *R* ↾ *S* in calculating **greedy algorithms** from specifications expressed by Galois connections).

Back to VFS — the *replay* function

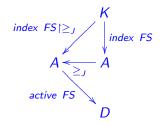
Finally, power-loss recovery is performed by the so-called *replay* function,

 $replay(FS, FI, J) \triangleq (active FS) \triangleleft (index FS \upharpoonright (\geq_J))$

where there is a final stage of filtering deleted keys out resorting to another combinator

 $S \triangleleft R \triangleq S^{\circ} \cdot \top \cap R$

which picks that part of R which "chains" with S. (Read $S \triangleleft R$ as "R if S is defined"; \cap denotes relation intersection.)



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Alloy

Ensures that, at any time, *replay* recovers the current *RI*,

 $ci(FS, FI, RI, J) \triangleq RI = replay(FS, FI, J) \land$ J is injective \land $eqdef(J^{\circ}, FS)$

where

- J injective ensures ≥_J anti-symmetric (cf. *RI* deterministic);
- eqdef(R, S) ensures that R and S are equally defined.

The last requirement is still too strong: J is bound to cover the whole FS at any time and thus power-loss recovery of RI by the *replay* function will thus take longer and longer as J grows.

FLASH index (FI) gets into the play

- From time to time, *J* should be cleared up while saving the contents of *RI* persistently.
- Such is the purpose of *FI* (flash index), a component of the state model which has played no role in the model so far.
- In this way, J will keep only the "difference" between the *RI* and its *cache FI*, very often outdated.
- Introducing *FI* requires a *commit* operation which basically updates the *FI* with the contents of *RI* and clears *J*, as specified by post-condition:

 $J' = \bot$ FI' = RI FS remains unchanged RI remains unchanged

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Caching *RI* into *FI* adds further complexity to the *replay* operation,

 $replay(FS, FI, J) = (active FS) \triangleleft (FI \dagger ((J^{\circ} \triangleleft (index FS)) \restriction (\geq_J)))$

— where † denotes relational overriding — but has the advantage of replaying only the operations that happened after the last check point (*commit*).

Pragmatics: need for extra term $J^{\circ} \triangleleft$ in the definition is quite subtle: it was prompted to us by a painful counter-example generated by the Alloy model-checker.

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Device full (garbage collection)

Specified by post-condition,

J, FI, RI remains unchanged $FS' = FS \cap \top \cdot RI^{\circ}$

this operation reclaims all FS entries inaccessible to RI, ie. those which mark deletions or outdated information, consequence of the **wear-levelling** principle. With points, the garbage-collected flash store is such that

 $x FS'a \Leftrightarrow x FS a \land (\exists k :: a RI k)$

Wear-leveling's implications in the model are clearly shown by the complexity of one of the usually most simple CRUD operations — *deletion* — to be given next.

Delete operation

Alloy:

```
pred Delete[cs,cs': CS, s: set K] {
   some n: K \rightarrow lone A, m: N \rightarrow lone A {
      injective[n, A] and injective[m, A]
      no n \cdot (cs \cdot fs) and n \cdot ran = m \cdot ran
      n \cdot dom = s and cs \cdot j \cdot Top \cdot (\mbox{m}) in (ordering/prev)
      cs' \cdot j = cs \cdot j + m
      cs' \cdot fs = cs \cdot fs + n \cdot del
      cs' \cdot ri = (cs \cdot fs \cdot dom - s) \cdot (cs \cdot ri)
      cs' \cdot fi = cs \cdot fi
```

where

 $\begin{array}{l} \mbox{fun del[n: K \rightarrow A] : A \rightarrow Entry} \\ \left\{ \begin{array}{l} \mbox{a: n \cdot ran, e: Entry } \mid e \cdot key \mbox{ in } n \cdot dom \mbox{ and } e \cdot value = DEL \end{array} \right\} \end{array}$



Life-cycle:

• This generates (among others) proof obligation:

```
assert po47 {

all cs,cs': CS, s : set K |

(ci[cs] and Delete[cs,cs',s]) \Rightarrow cs'·ri = cs'·replay

}
```

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- Alloy finds no counter-examples
- We thus proceed to manual proof (next slide)

Calculation

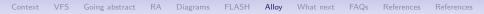
Nine (deductive) steps:

replay(FS', FI', J')

- $= \{ \text{ substitutions enabled by post-condition } \}$ replay(FS \cup del N, FI, J')
- $= \{ \text{ definition of } replay ; active \text{ distributes over union } \} \\ (active FS \cup active(del N)) \triangleleft (index (FS \cup del N) \upharpoonright (\geq_{J'})) \\ = \{ \text{ orthogonality ; } index \text{ distributes over union ; } del \} \\ (active FS) \triangleleft ((index FS \cup N) \upharpoonright (\geq_{I'})) \end{cases}$

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- $\begin{array}{l} \vdots \\ \{ 5 \text{ steps omitted for presentation purposes } \} \\ RI \cdot (\notin S) \cup \bot \end{array}$
- = { post-condition }



On the RA-Alloy interplay

Comment by a referee:

(...) difficult proof steps model-checked first ... seems like an excellent idea, but how do you know when to model-check, wait until your proof is running into trouble? If this is still a matter of good mathematical judgement, this should be made clear.

Our answer is a quite pragmatic design principle:

Whatever you are going to do in applied **RA**, model-check it first. Silly errors are very likely in complex designs — even using **RA** :-)

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(Recall design of final version of *replay*.)

Context VFS Going abstract RA Diagrams FLASH Alloy What next FAQs References References Lessons learnt

- Smart notation better than heavy machinery
- Abstraction as main ingredient of scientific engineering
- GCI the right way to go:
 - builds communities
 - enables comparative work
 - pushes you favourite method to its limits
 - has social impact

If you know of a GC in our area of work — just embrace it!

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From (another) referee:

(...) engineering practitioners have mathematical maturity but cannot be expected to be widely educated on abstract mathematics. (...) The authors need to plan on writing a textbook that explains the approach, in much the same manner as the various textbooks for Z, starting from first principles (ZF set theory and first order logic), if they really expect the approach to see significant industrial use.

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- Fine the book is (slowly) under way...
- Meanwhile, teaching experience has been gathered in the last three years in teaching RA (with applications) to MSc students, as a module of the MFES ("Métodos Formais em Engenharia de Software") unit — slides available from

wiki.di.uminho.pt/twiki/bin/view/Education/MFES/

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• Interested in **relational thinking**? Join the club — you are already 150 years late! (2010-1860=150)



Question: do you advocate returning to manual proofs, full stop?

Answer: No — the size and complexity of today's problems make hand-proving alone unrealistic. What is advocated is **relational thinking**, a change in the way we think about software.

Question: why Alloy? Couldn't other model-checkers be used instead?

Answer: *Hmm...* perhaps not — Alloy's main inspiration is Tarski's stuff (much more relevant than the Z inspiration). In particular, Alloy's pointfree subset is very close do **RA**.

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Questions and answers

Question: how reliable (formal) is your translation between **RA** and Alloy?

Answer: Manual but systematic; a translation tool has just become available :-)

N. Macedo Translating Alloy specifications to the point-free style. Master's thesis, Minho University, 2010. (Submitted.)

Question: does theorem proving (TP) still find a place in your approach?

Answer: Of course it does! But the level of proofs needs to raise up to **RA**-styled proofs. Höfner and Struth (2008) show that TP such as **Prover9** already work at this level.

Questions and answers

Question: is **RA** expressive enough to tackle "big" problems such as those of the GCI?

Answer: Yes – and it gives you room to "invent" new combinators (such as $R \upharpoonright S$) and exploit their algebra — this saves much work and structures the reasoning.

Question: what is the relationship between **RA** and the database homonym "à la Codd"?

Answer: Codd's multi-ary relation theory instantiates **RA**, once its set-theoretic foundations are "pointfreed"

Question: can other families of logics, for instance **temporal logic** be pointfreed in **RA**?

Answer: Yes – Raymond Boute (2009) got a best paper award at FM'09 (Eindhoven World Congress) doing so.

Questions and answers

Question: what about probabilistic modelling, Markov chains and the like?

Answer: RA is in many respects a linear-algebra (LA), as binary relations are just Boolean matrices. Composition instantiates matrix multiplication and converse matrix transposition. So RA is much closer to LA than predicate logic.

Question: you rely much on diagrams; could UML diagrams be used instead?

Answer: UML diagrams are informal and thus hard to reason about; our diagrams are central to the underlying **allegory** theory — they can even be used as proofs (Freyd and Scedrov, 1990).

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Context VFS Going abstract RA Diagrams FLASH Alloy What next FAQs **References** References

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